

FIG. 14

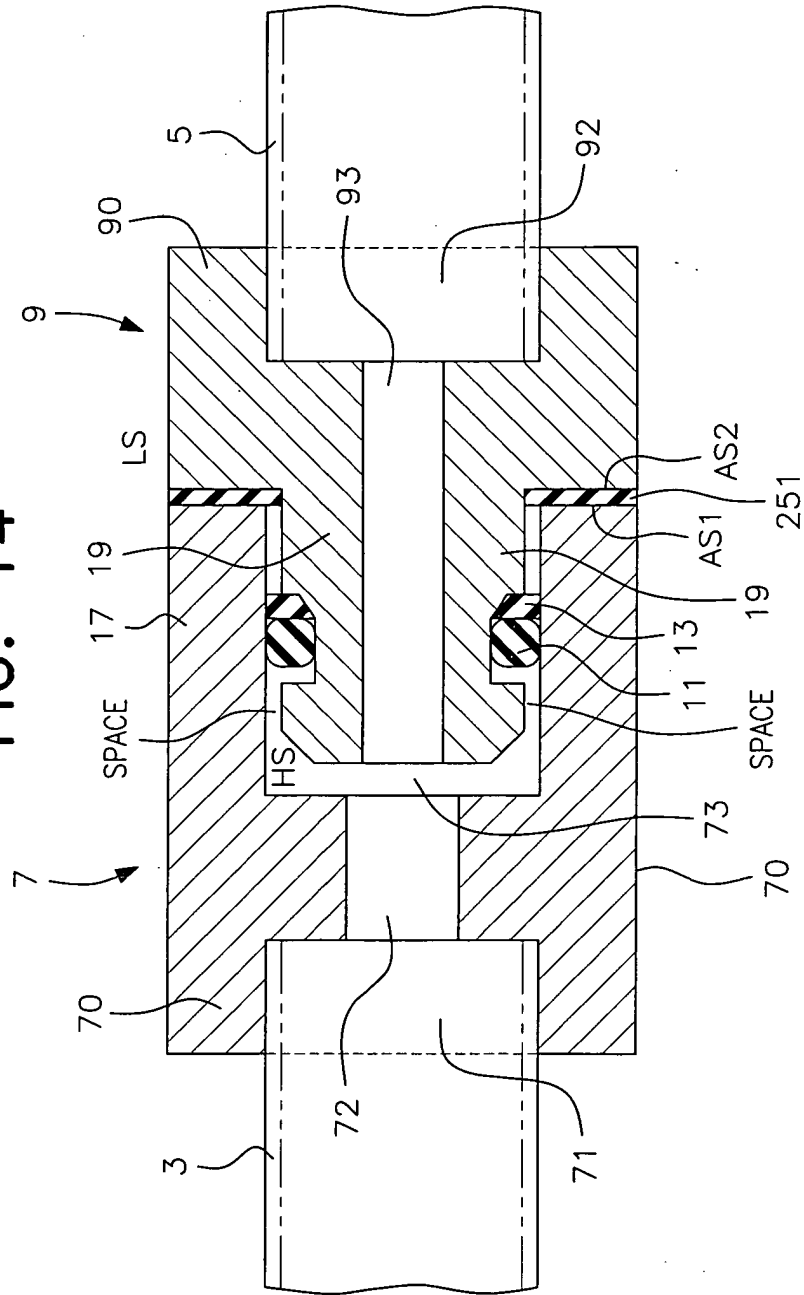


FIG. 15

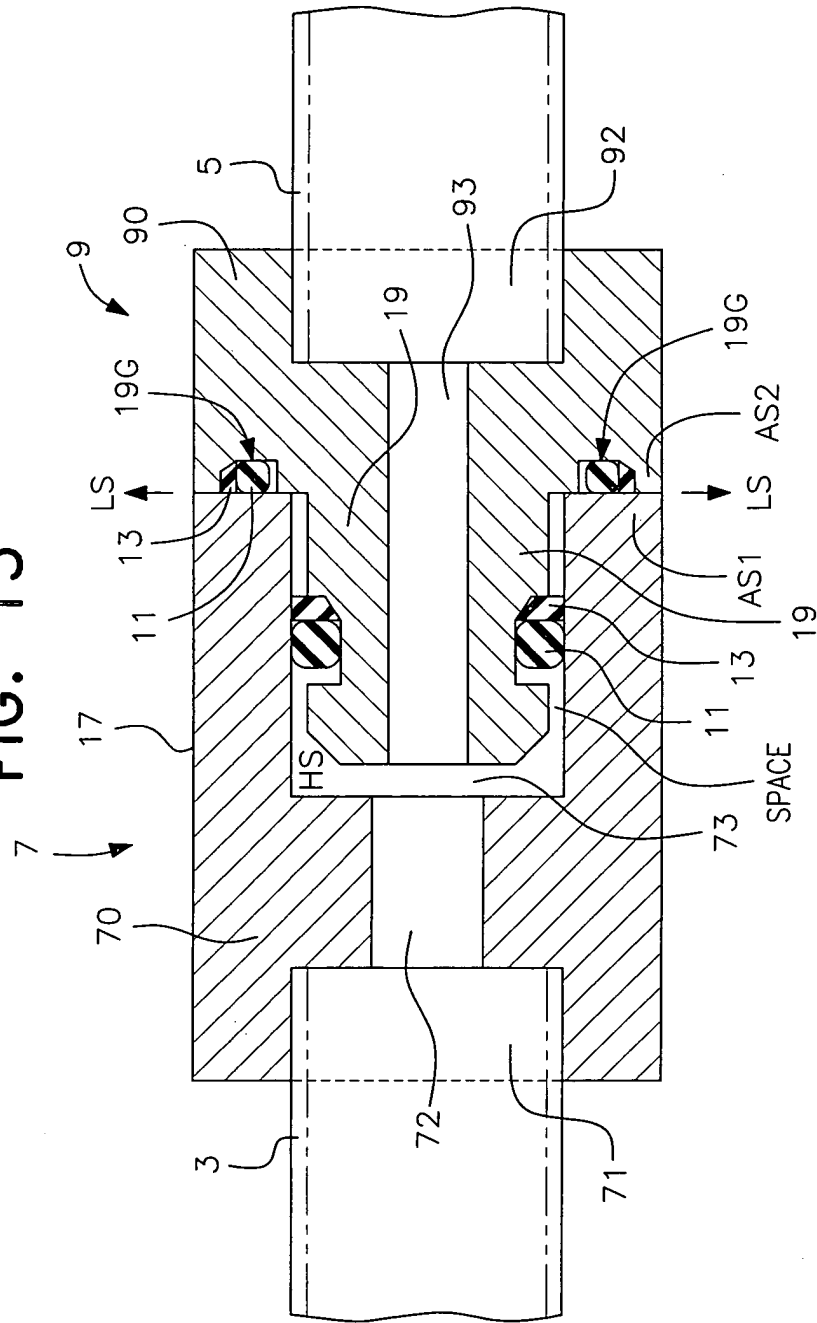


FIG. 1

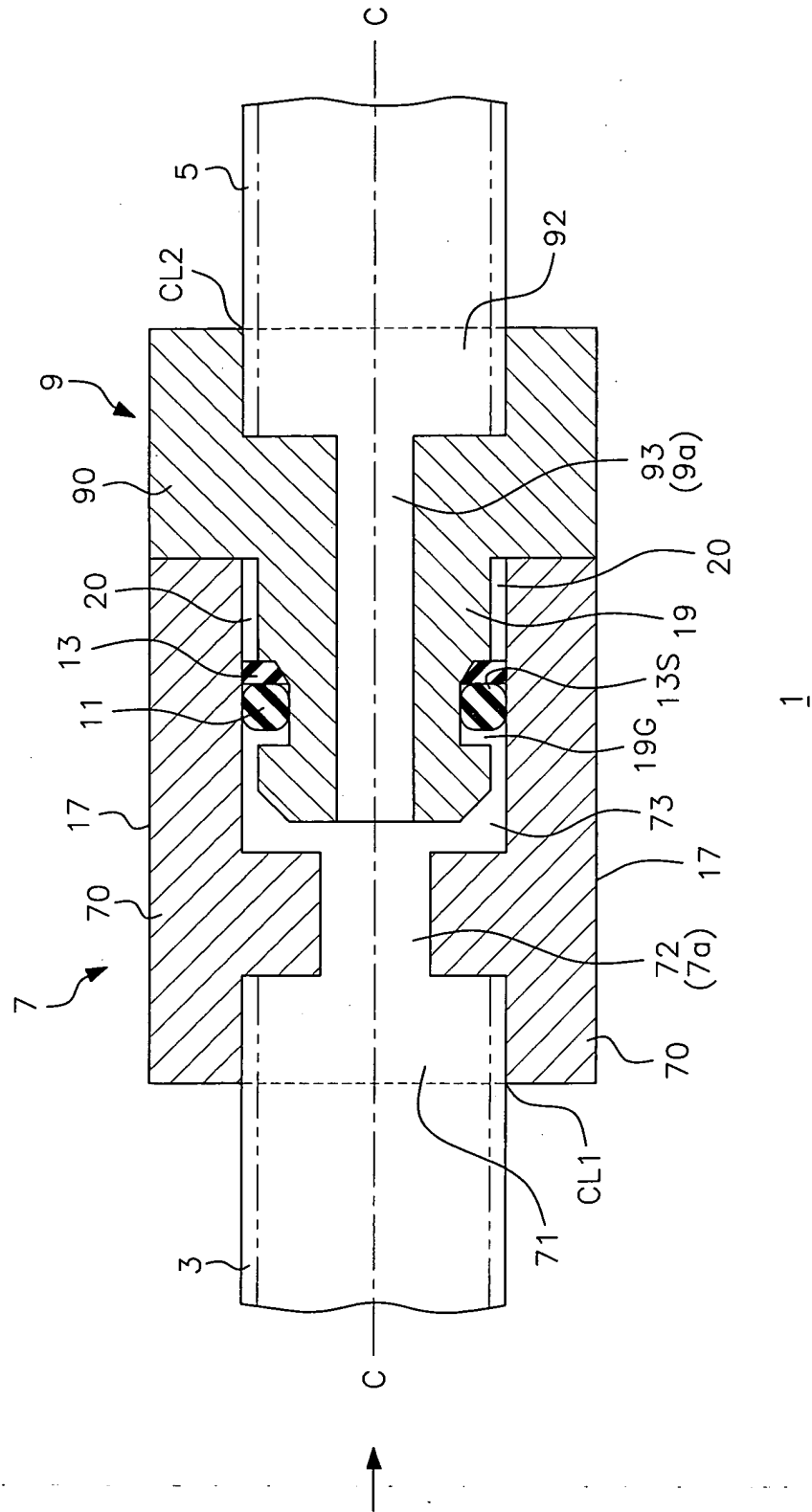


FIG. 2

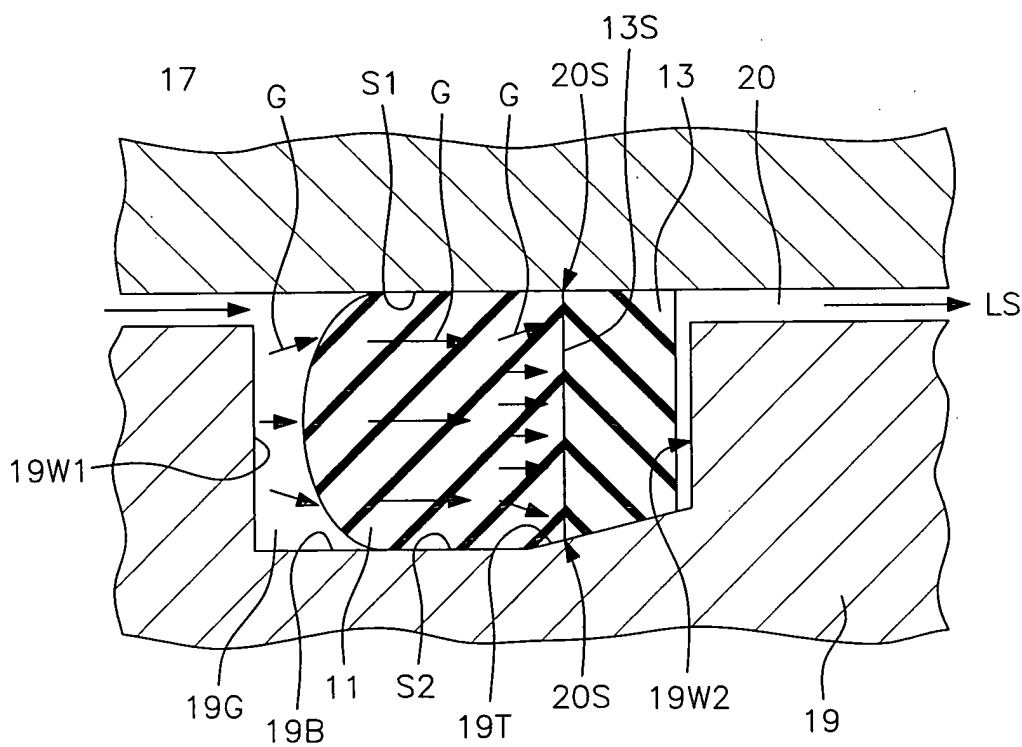


FIG. 3A



FIG. 3B

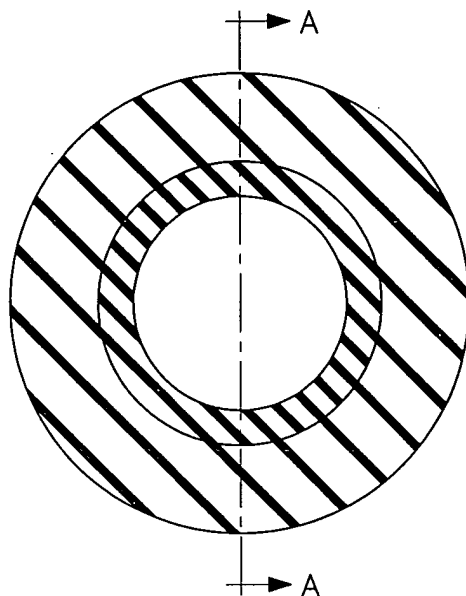


FIG. 3C

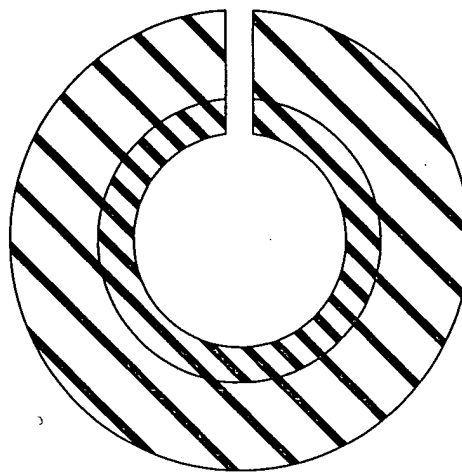
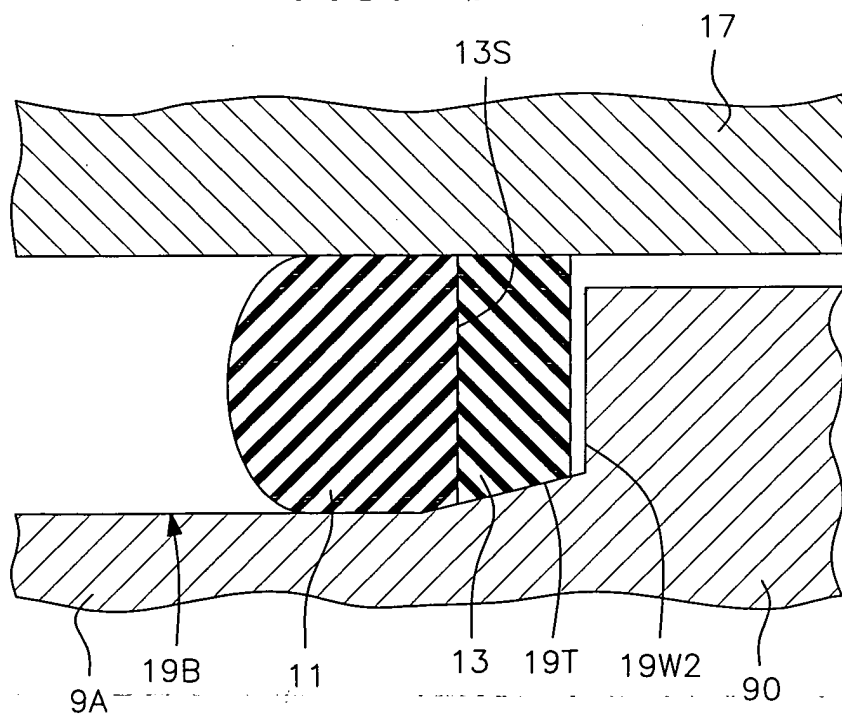


FIG. 4



This diagram is a cross-sectional view of a semiconductor device. It features a central region 73 with a chevron pattern, likely representing a crystal grain boundary or a specific material layer. This central region is surrounded by several layers: 17B (top left), 17W (top right), 17a (top right), 19B (bottom left), 19W11 (bottom left), 19a (bottom left), 19B (bottom center), 19T (bottom center), and 19W21 (bottom right). Arrows indicate directions HS (Horizontal Surface) and LS (Longitudinal Surface).

[illegible]

FIG. 6A

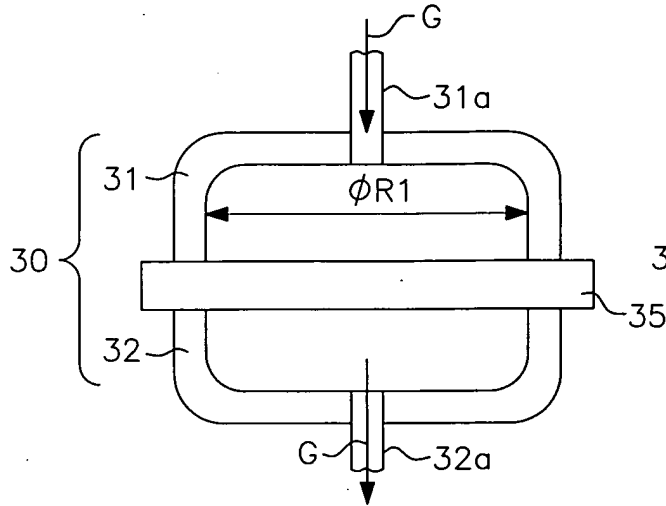


FIG. 6B

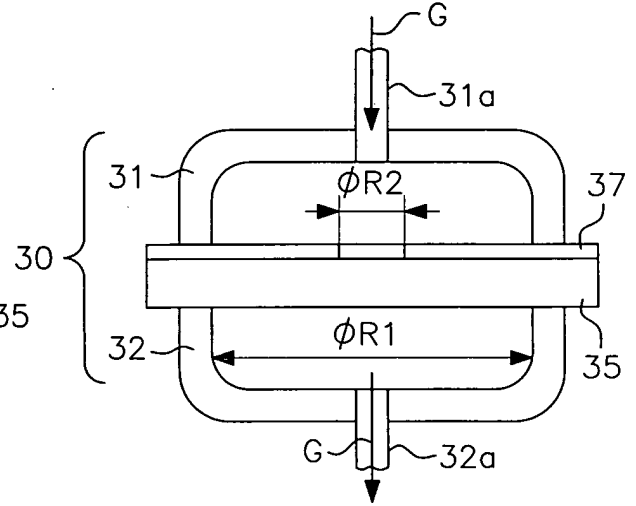


FIG. 6C

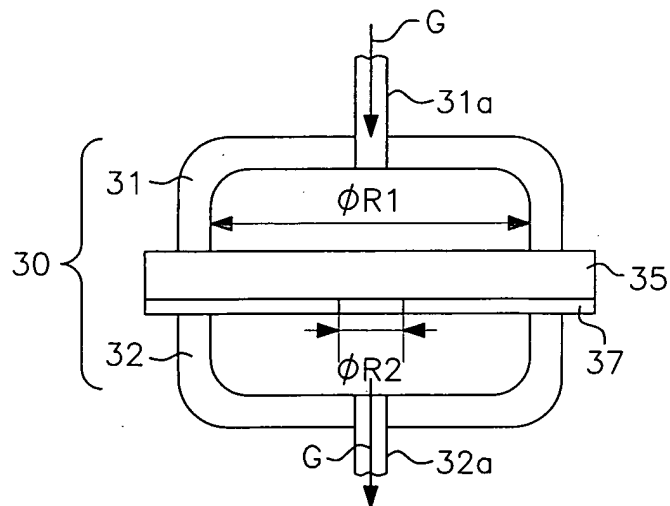


FIG. 7A

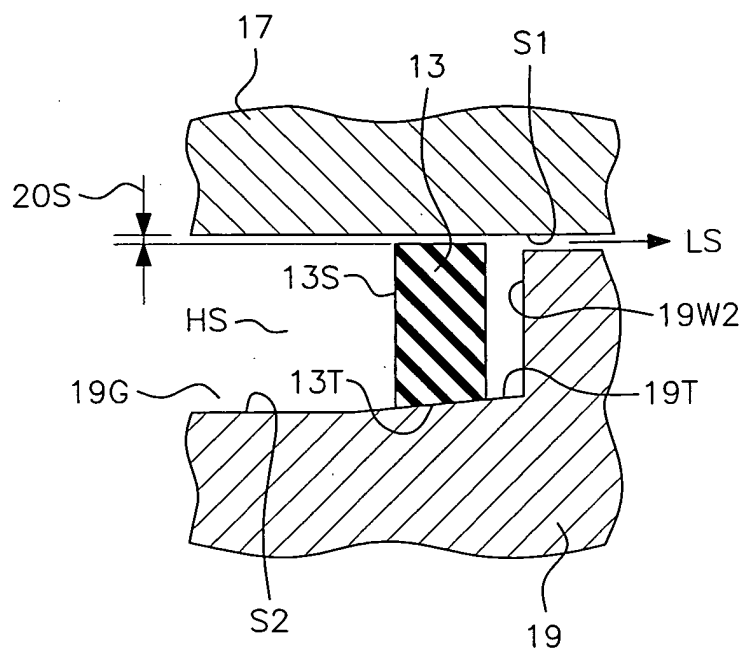


FIG. 7B

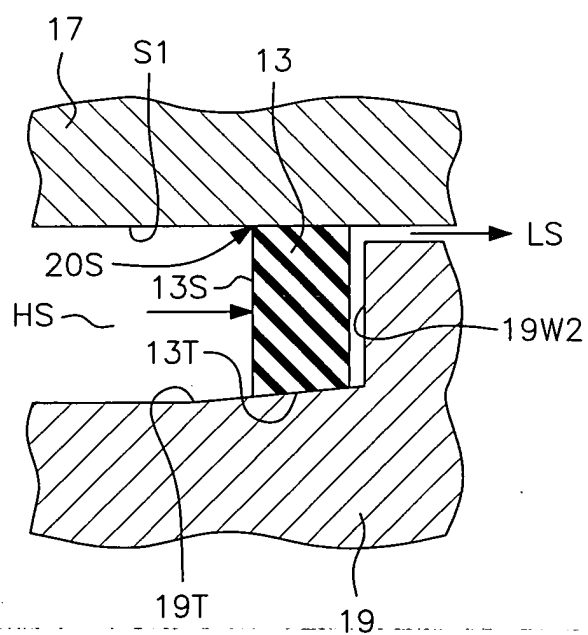


FIG. 8A

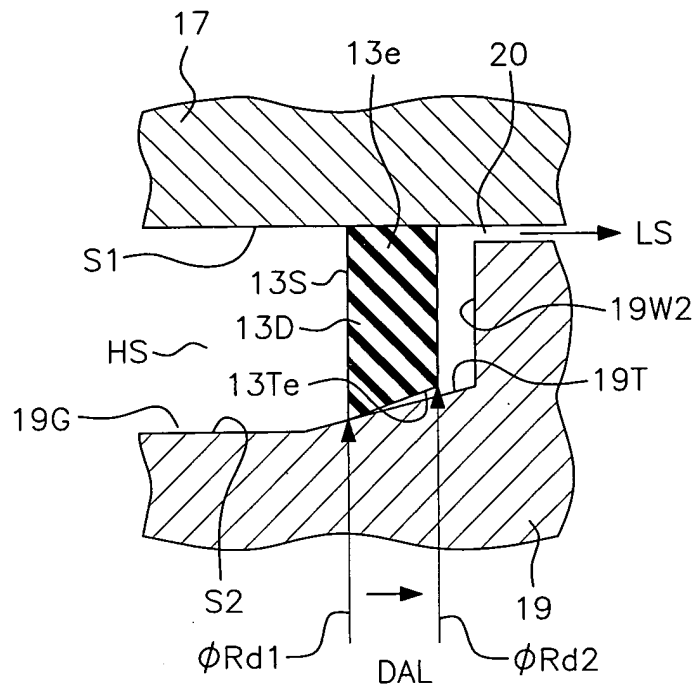


FIG. 8B

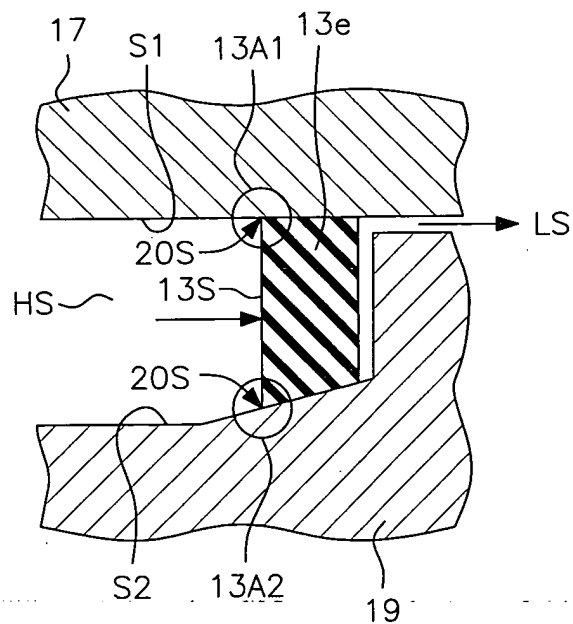


FIG. 9A

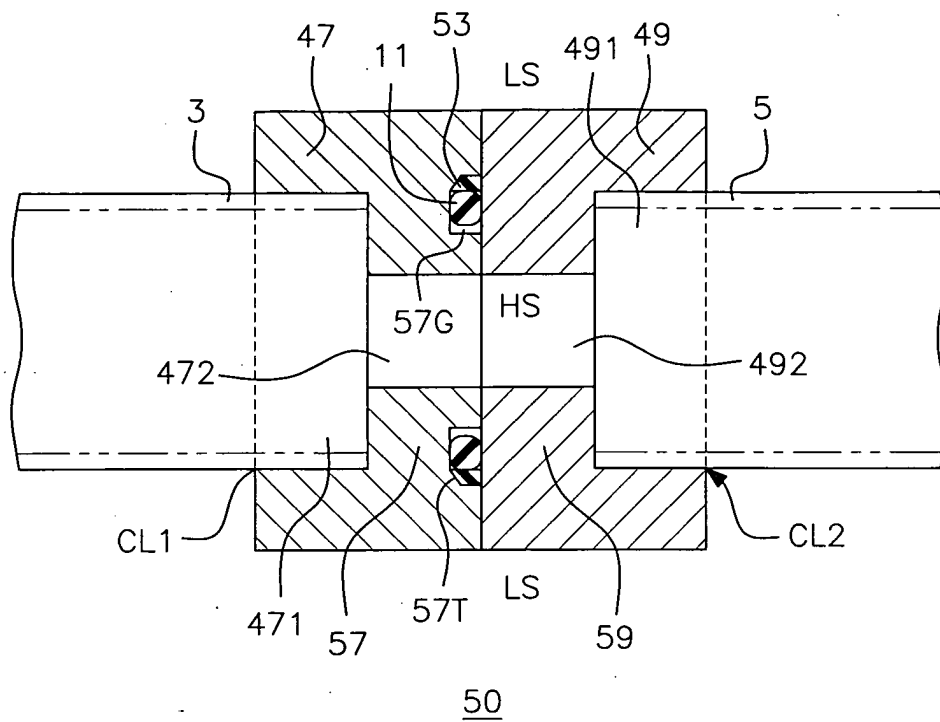


FIG. 9B

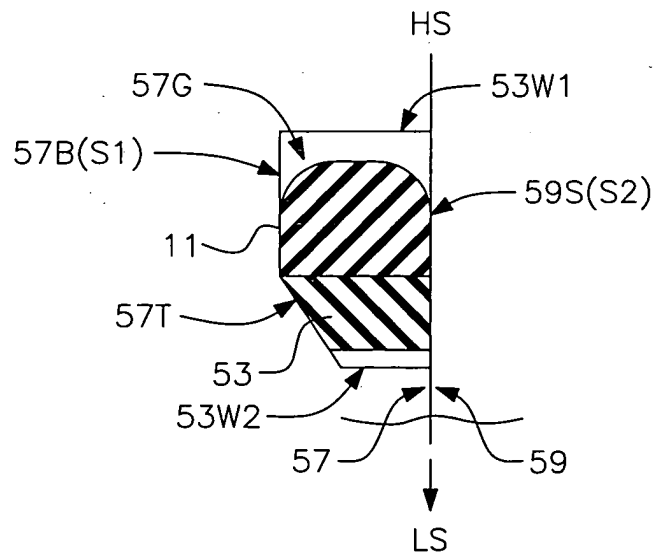


FIG. 10

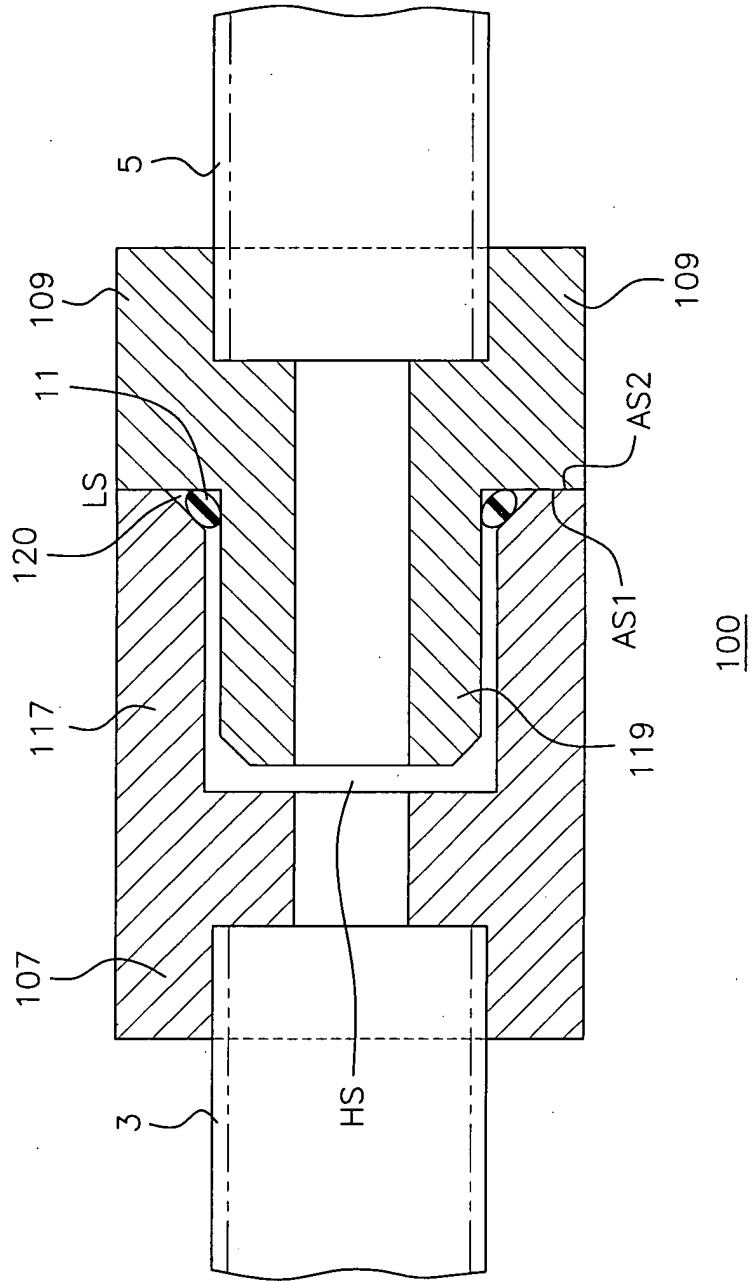


FIG. 11

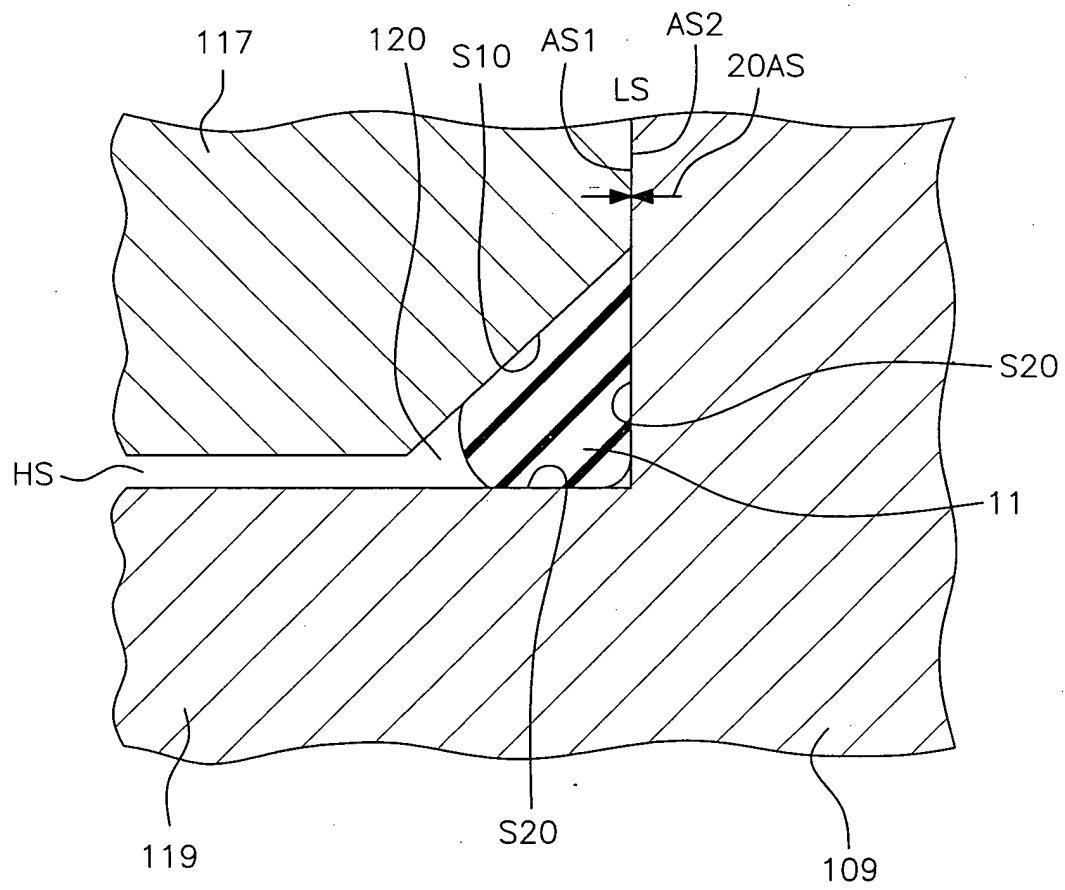


FIG. 12

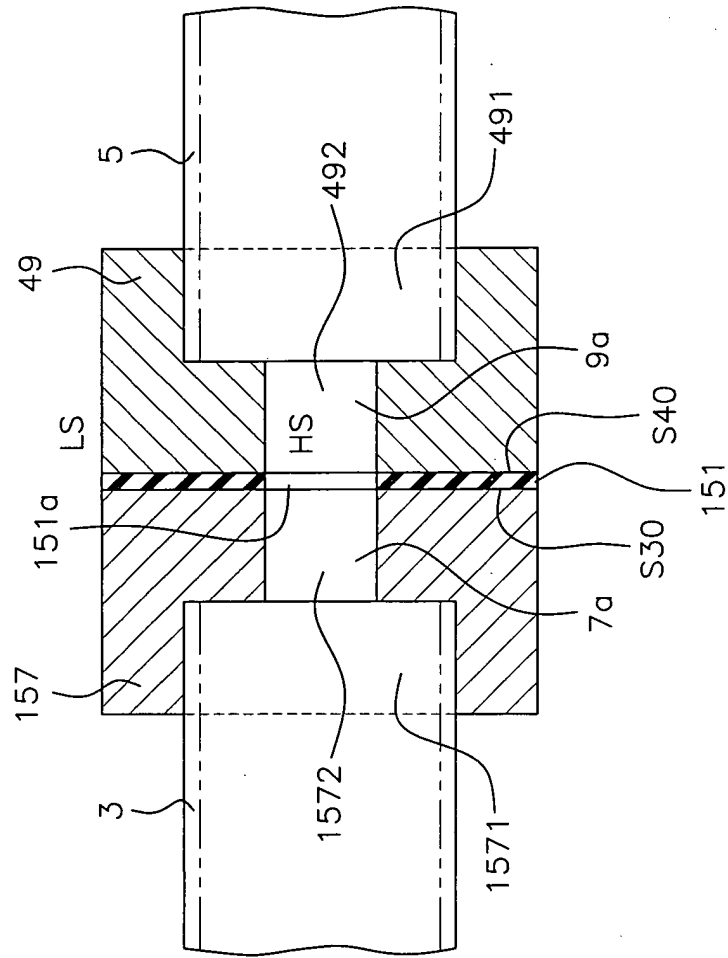


FIG. 13

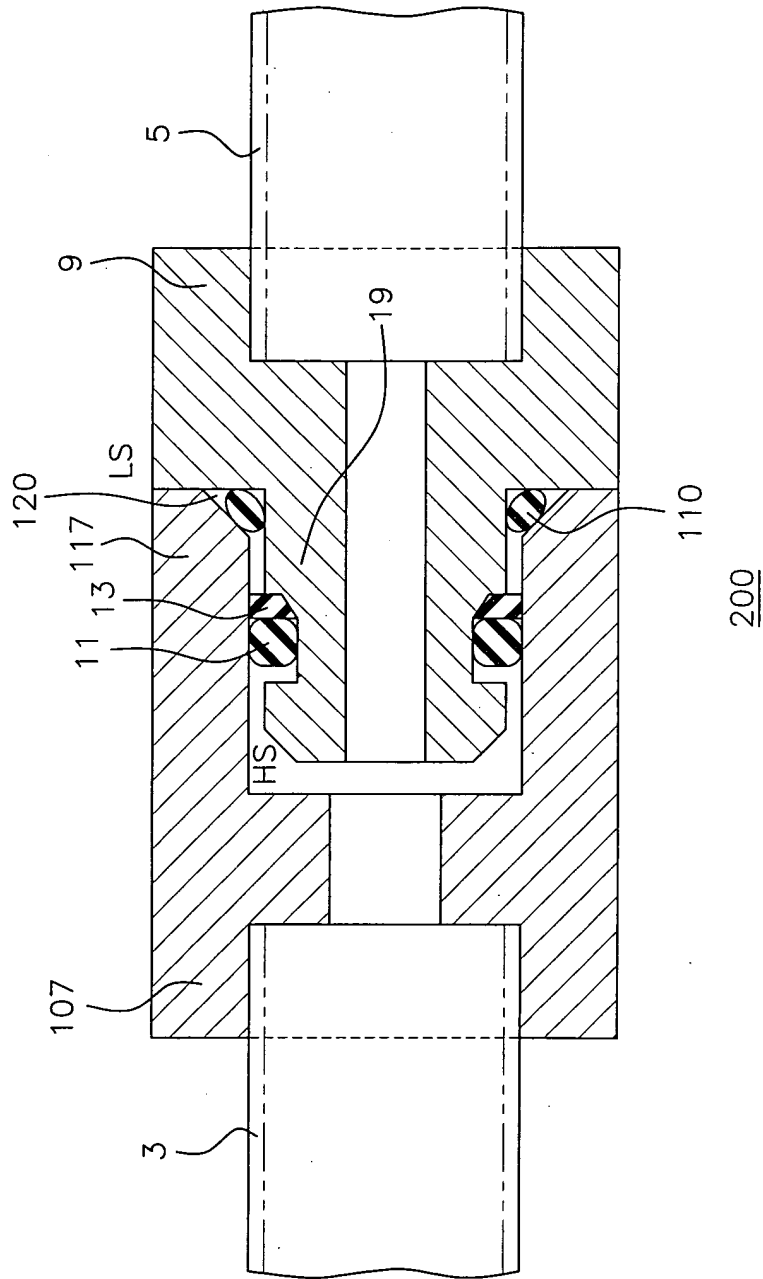


FIG. 14

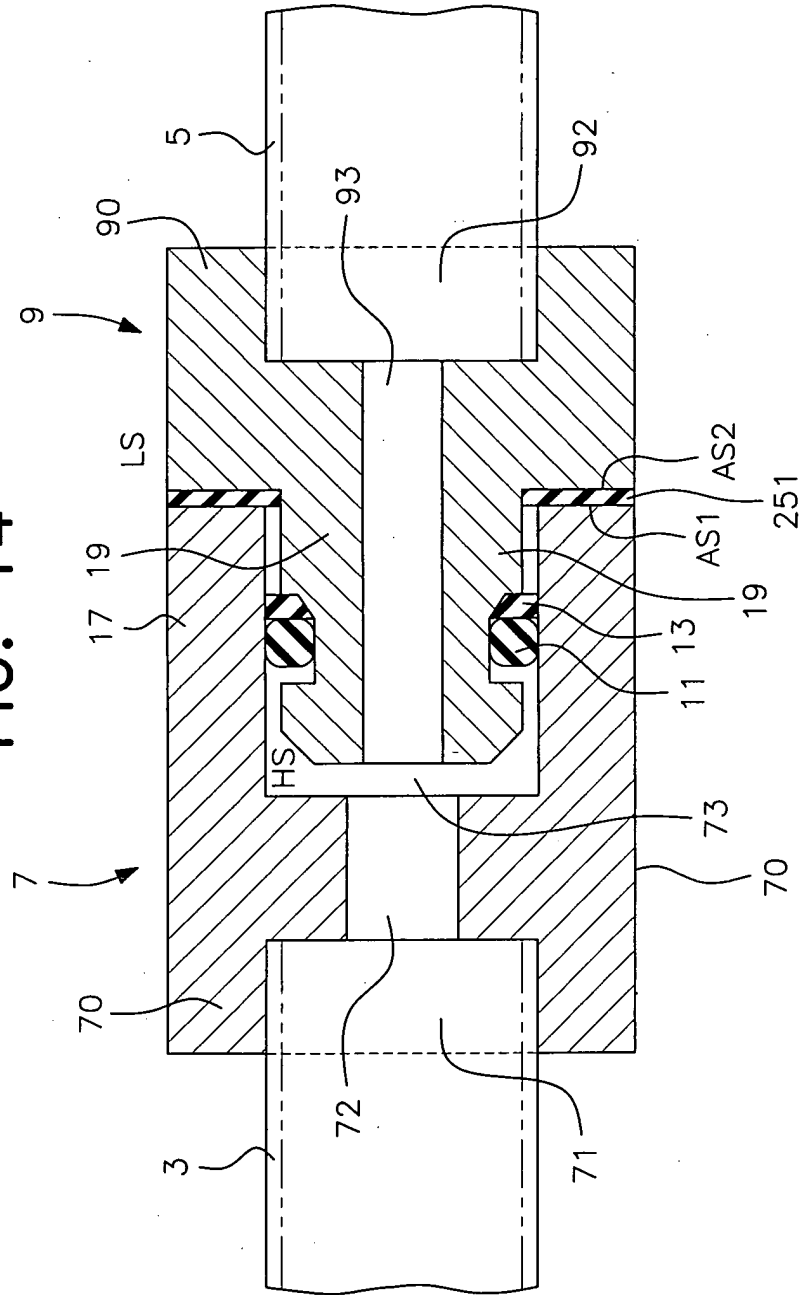


FIG. 15

